

Amendments to the Claims:

This listing of claims replaces all prior versions, and listings, of claims in this application.

Listing of Claims:

1-9. (Canceled)

10. (Currently Amended) A production method for an electronic apparatus comprising a board having a separable region, said board being mounted with a central processing unit, an electrically alterable nonvolatile storage device, a connector, a scrambling device, and a detection device for detecting whether said region is separated or not, wherein:

when said region is not separated yet, data read out of said storage device can be descrambled with a first scrambling pattern by said scrambling device and the descrambled data can be transferred to said central processing unit, and by connecting an external apparatus to said connector and directly controlling an internal logic circuit of said central processing unit, said central processing unit can be caused to output data to said scrambling device and said scrambling device can scramble the output data of said central processing unit with said first scrambling pattern and write the scrambled data to said storage device; and

when said region is separated, based on an output signal of said detection device, data read out of said storage device can be descrambled with said first scrambling pattern by said scrambling device and the descrambled data can be transferred to said central processing unit, and by connecting said external apparatus to said connector and directly controlling the internal logic circuit of said central processing unit, said central processing unit can be caused to output data to said scrambling device and said scrambling device can scramble the output data of said central processing unit with a second scrambling pattern and write the scrambled data to said storage device,

said production method comprising:

a writing step in which by connecting said external apparatus to said connector and directly controlling the internal logic circuit of said central processing unit, said central processing unit is caused to output data to said scrambling device, and in which said scrambling

device scrambles the output data of said central processing unit with the first scrambling pattern and writes the scrambled data to said storage device; and
a separating step of separating said region after said writing step.

11. (Original) A production method for an electronic apparatus according to claim 10, wherein said scrambling device, said detection device, and other circuits essential to the operation of said electronic apparatus are contained in a single semiconductor device.

12. (Currently Amended) A production method for an electronic apparatus according to claim 11, wherein said semiconductor device is sealed in such a manner that, when mounted on said board, terminals of said semiconductor device cannot be touched from the outside.

13-14. (Canceled)

15. (Original) A production method for an electronic apparatus according to claim 10, wherein said board is a multi-layer board consisting of at least four layers, and a line used to carry a detection signal indicating whether said region is separated or not is formed in an inner layer of said board.

16-17. (Canceled)

18. (Original) A production method for an electronic apparatus according to claim 10, wherein said board is a multi-layer board consisting of at least four layers, and at least one connecting line connecting between said central processing unit and said relaying device is formed in an inner layer of said board.

19-22. (Canceled)

23. (Original) An electronic apparatus comprising a board having a separable region, said board being mounted with a central processing unit, an electrically alterable nonvolatile storage

device, a connector, a scrambling device, and a detection device for detecting whether said region is separated or not, wherein:

when said region is not separated yet, data read out of said storage device can be descrambled with a first scrambling pattern by said scrambling device and the descrambled data can be transferred to said central processing unit, and by connecting an external apparatus to said connector and directly controlling an internal logic circuit of said central processing unit, said central processing unit can be caused to output data to said scrambling device and said scrambling device can scramble the output data of said central processing unit with said first scrambling pattern and write the scrambled data to said storage device; and

when said region is separated, data read out of said storage device can be descrambled with said first scrambling pattern or with a second scrambling pattern different from said first scrambling pattern by said scrambling device, and the descrambled data can be transferred to said central processing unit, and by connecting said external apparatus to said connector and directly controlling the internal logic circuit of said central processing unit, said central processing unit can be caused to output data to said scrambling device and said scrambling device can scramble the output data of said central processing unit with said second scrambling pattern and write the scrambled data to said storage device.

24. (Original) An electronic apparatus comprising a board having a first separable region and a second separable region, said board being mounted with a central processing unit, an electrically alterable nonvolatile storage device, a scrambling device, a detection device for detecting whether said first region is separated or not, and a detection device for detecting whether said second region is separated or not, wherein:

when neither said first region nor said second region is separated yet, data read out of said storage device can be descrambled with a first scrambling pattern by said scrambling device and the descrambled data can be transferred to said central processing unit, and by connecting an external apparatus to said connector and directly controlling an internal logic circuit of said central processing unit, said central processing unit can be caused to output data to said scrambling device and said scrambling device can scramble the output data of said central

processing unit with said first scrambling pattern and write the scrambled data to said storage device;

when said first region is separated but said second region is not separated yet, data read out of said storage device can be descrambled with said first scrambling pattern by said scrambling device and the descrambled data can be transferred to said central processing unit, and by connecting said external apparatus to said connector and directly controlling the internal logic circuit of said central processing unit, said central processing unit can be caused to output data to said scrambling device and said scrambling device can scramble the output data of said central processing unit with a second scrambling pattern, a scrambling pattern different from said first scrambling pattern, and write the scrambled data to said storage device; and

when said first region and said second region are both separated, data read out of said storage device can be descrambled with said second scrambling pattern by said scrambling device and the descrambled data can be transferred to said central processing unit, but data cannot be written to said storage device via said scrambling device by directly controlling the internal logic circuit of said central processing unit through said external apparatus connected to said connector.

25. (Canceled)